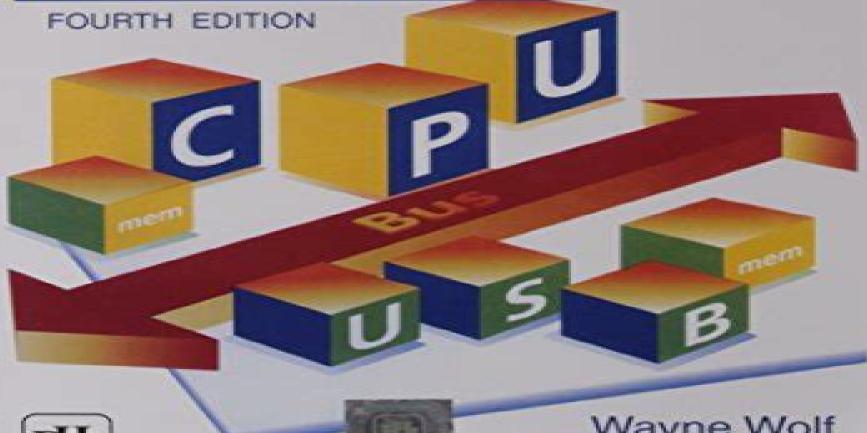


# Modern VLSI Desig

IP-BASED DESIGN







Wayne Wolf

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