

MODULE 5

Data Processing Instructions

- The data processing instructions manipulate data within registers. They are move instructions, arithmetic instructions, logical instructions, compare instructions and multiply instructions.
- Most data processing instructions can process one of their operands using the barrel shifter.
- If *S* is suffixed on a data processing instruction, then it updates the flags in the cpsr.

MOVE INSTRUCTIONS:

- It copies *N* into a destination register *Rd*, where *N* is a register or immediate value. This instruction is useful for setting initial values and transferring data between registers.

Syntax: <instruction> {<cond>} {S} Rd, N

MOV	Move a 32-bit value into a register	$Rd = N$
MVN	move the NOT of the 32-bit value into a register	$Rd = \sim N$

- In the example shown below, the MOV instruction takes the contents of register *r5* and copies them into register *r7*.

```

PRE   r5 = 5
      r7 = 8
      MOV r7, r5 ; let r7 = r5
POST  r5 = 5
      r7 = 5

```

USING BARREL SHIFTER WITH DATA TRANSFER INSTRUCTION:

- Data processing instructions are processed within the arithmetic and logic unit (ALU).
- A unique and powerful feature of the ARM processor is the ability to shift the 32-bit binary pattern in one of the source registers left or right by a specific number of positions before it enters the ALU.
- This shift increases the power and flexibility of many data processing operations.
- For example, We apply a logical shift left (LSL) to register *Rm* before moving it to the destination register.

```

PRE   r5=5
      r7=8
      MOV r7, r5, LSL #2
POST  r5=5
      r7=20

```

- The above example shift logical left *r5=5* (00000101 in binary) by two bits and then *r7=20* (00010100 in binary).

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Table of Contents Microcontroller Lab Manual Vtu 4th Sem

1. Understanding the eBook Microcontroller Lab Manual Vtu 4th Sem
 - The Rise of Digital Reading Microcontroller Lab Manual Vtu 4th Sem
 - Advantages of eBooks Over Traditional Books
2. Identifying Microcontroller Lab Manual Vtu 4th Sem
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microcontroller Lab Manual Vtu 4th Sem
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microcontroller Lab Manual Vtu 4th Sem
 - Personalized Recommendations
 - Microcontroller Lab Manual Vtu 4th Sem User Reviews and Ratings
 - Microcontroller Lab Manual Vtu 4th Sem and Bestseller Lists

5. Accessing Microcontroller Lab Manual Vtu 4th Sem Free and Paid eBooks
 - Microcontroller Lab Manual Vtu 4th Sem Public Domain eBooks
 - Microcontroller Lab Manual Vtu 4th Sem eBook Subscription Services
 - Microcontroller Lab Manual Vtu 4th Sem Budget-Friendly Options
6. Navigating Microcontroller Lab Manual Vtu 4th Sem eBook Formats
 - ePub, PDF, MOBI, and More
 - Microcontroller Lab Manual Vtu 4th Sem Compatibility with Devices
 - Microcontroller Lab Manual Vtu 4th Sem Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microcontroller Lab Manual Vtu 4th Sem
 - Highlighting and Note-Taking Microcontroller Lab Manual Vtu 4th Sem
 - Interactive Elements Microcontroller Lab Manual Vtu 4th Sem
8. Staying Engaged with Microcontroller Lab Manual Vtu 4th Sem
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microcontroller Lab Manual Vtu 4th Sem
9. Balancing eBooks and Physical Books Microcontroller Lab Manual Vtu 4th Sem
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microcontroller Lab Manual Vtu 4th Sem
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Microcontroller Lab Manual Vtu 4th Sem
 - Setting Reading Goals Microcontroller Lab Manual Vtu 4th Sem
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Microcontroller Lab Manual Vtu 4th Sem
 - Fact-Checking eBook Content of Microcontroller Lab Manual Vtu 4th Sem
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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